

**FIG. 9**

Diagram illustrating a network topology. A central horizontal line (99) connects eight nodes labeled PC (1'). Above the line, a node labeled PC (1) is connected via a vertical line (99). To the right, a node labeled I (3) is connected via a vertical line (99), with an arrow pointing towards the node.

The diagram illustrates a computer system architecture. On the left, a vertical stack of components is shown: HD (Hard Drive), FD (Floppy Disk), CDR/W (Compact Disc Read/Write), DVD (Digital Versatile Disc), RAM (Random Access Memory), and a blank box labeled 68. To the right of this stack is a section labeled 30, containing M (Motherboard), CD-ROM, FM (Frequency Modulation), a blank box, and a blank box labeled 69. A central vertical line separates these from the right side. On the right side, there is a vertical stack of components: S<sub>1</sub>, S<sub>3</sub>, RAM, a blank box labeled 66, HD (Hard Drive) labeled 61', DVD, and a blank box labeled 64'. To the right of this stack is another vertical stack: S<sub>2</sub>, S<sub>4</sub>, a blank box labeled 60, a blank box labeled 70, and CD (Compact Disc) labeled 63'. A horizontal line labeled 1, PC connects the top of the right side to a circle labeled NS (Network Server). A dashed line connects NS to a circle labeled I (Internet). A line labeled 99 connects the bottom of the right side to the NS. Labels at the bottom identify the components: GRAPHICS CARD (pointing to 68), SOUND CARD (pointing to 69), TELEVISION RECEIVER (pointing to 64'), and DIGITAL SIGNAL PROCESSOR (pointing to 89). Other labels include 60, 70, 63', and 66, which are associated with the blank boxes in the right side stack.

**FIG. 10C**  
90, PC MICROCHIP  
(PC ON A CHIP)

1 MODEM

2 NETWORK COMMUNICATIONS

3 TELEVISION

93 M

50 DISPLAY

66 RAM

94 S

94 S

94 S

94 S

87

85

89, DIGITAL SIGNAL PROCESSOR

99

NS

I

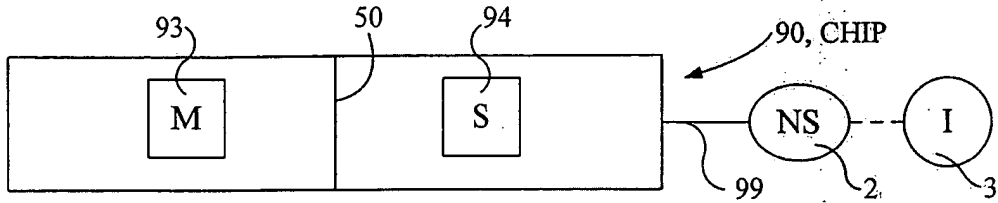


FIG. 10D

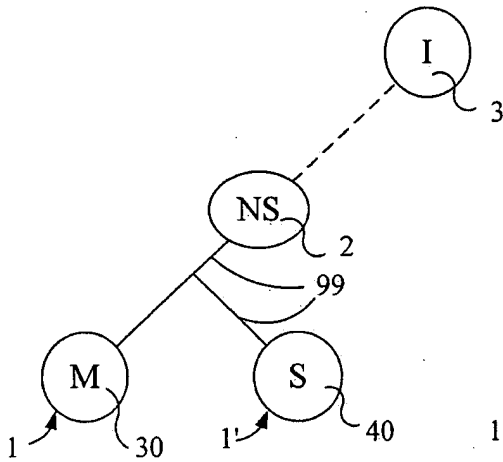


FIG. 10E

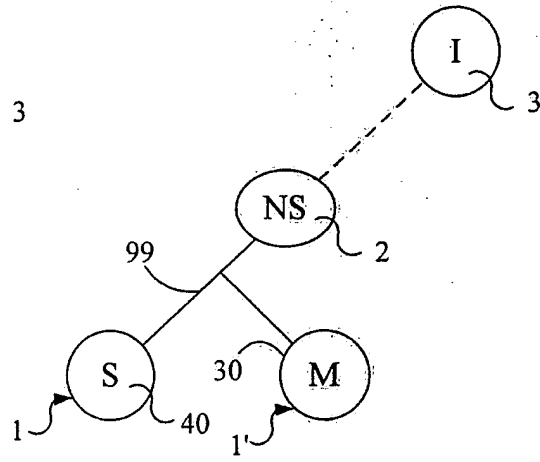


FIG. 10F

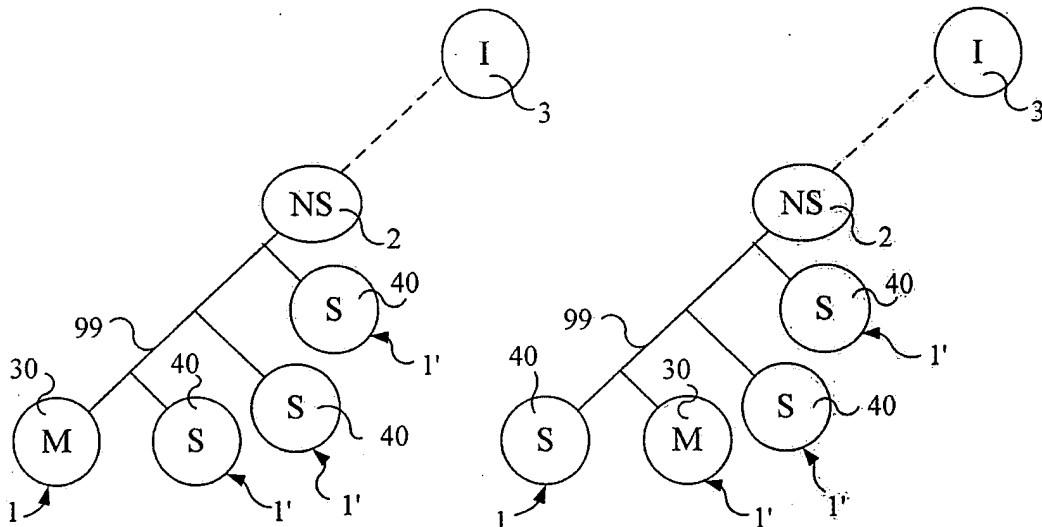


FIG. 10G

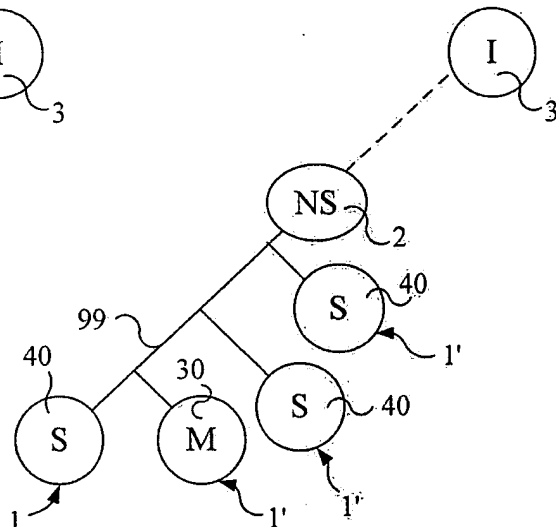


FIG. 10H

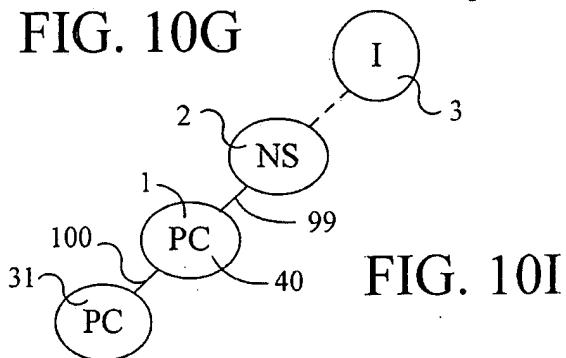


FIG. 10I

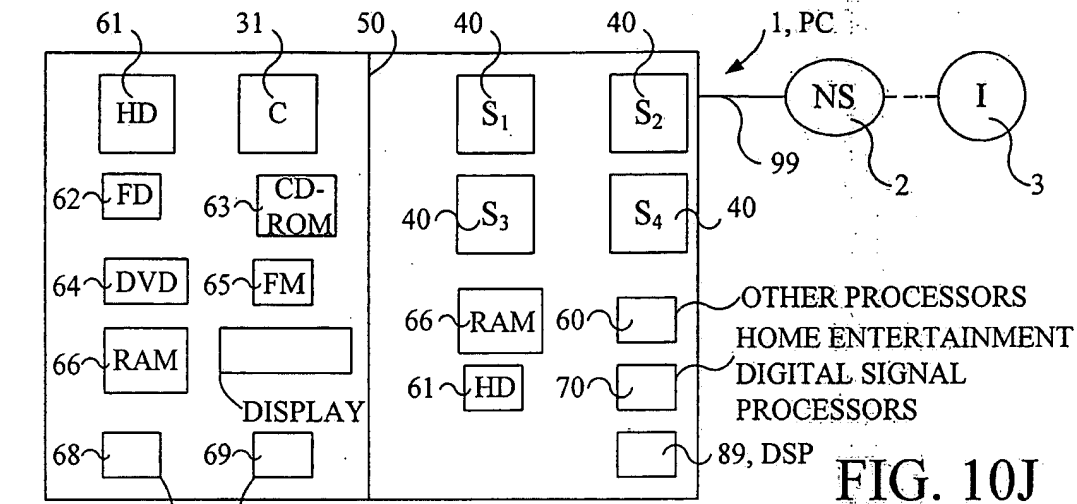


FIG. 10J

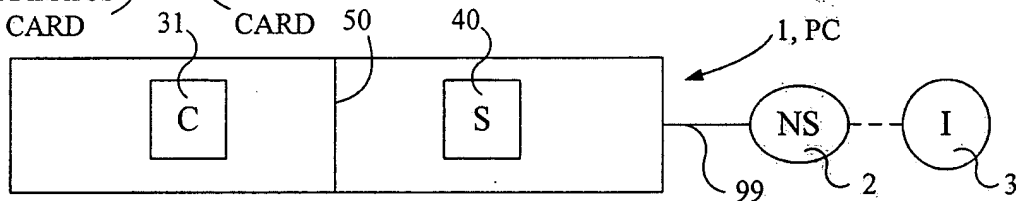


FIG. 10K

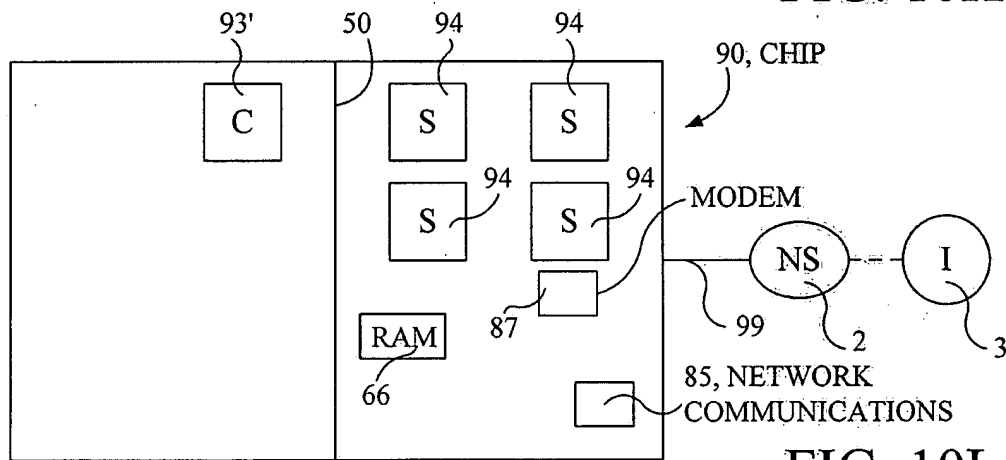


FIG. 10L

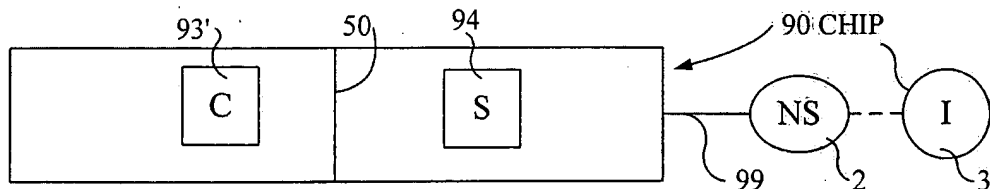


FIG. 10M

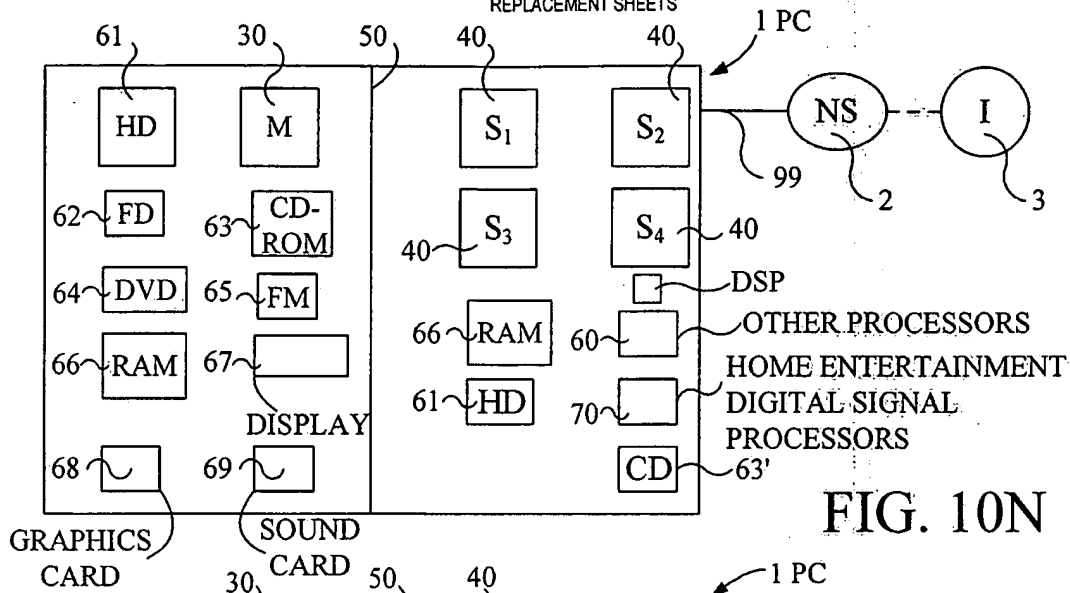


FIG. 10N

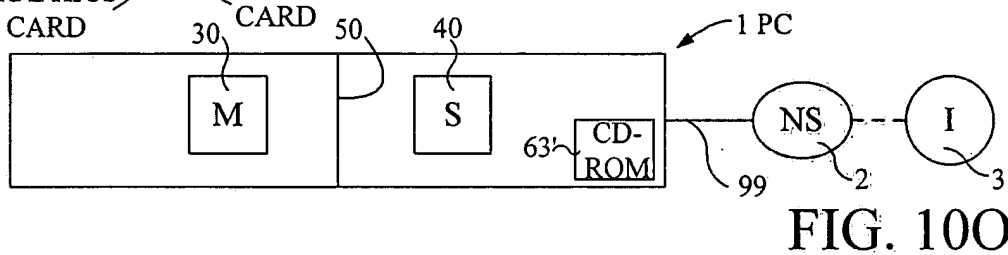


FIG. 10O

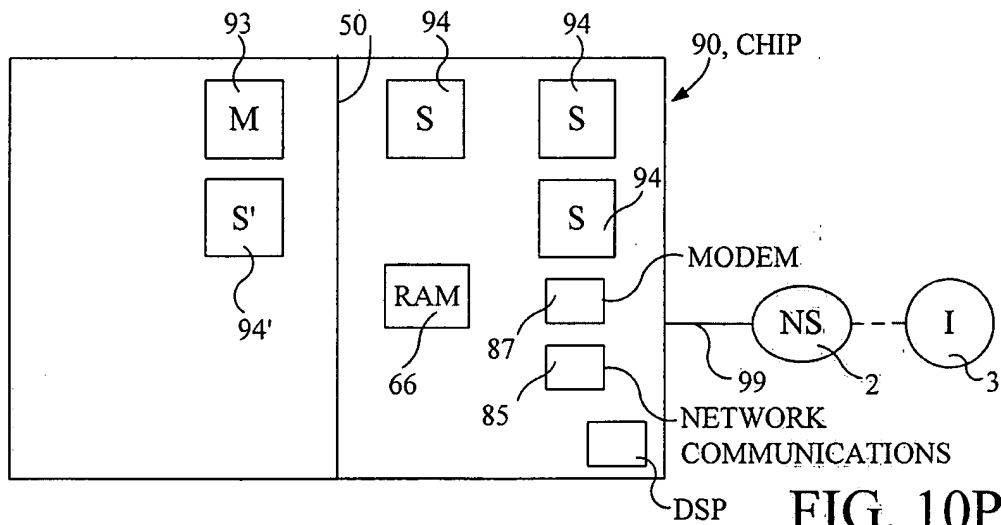


FIG. 10P

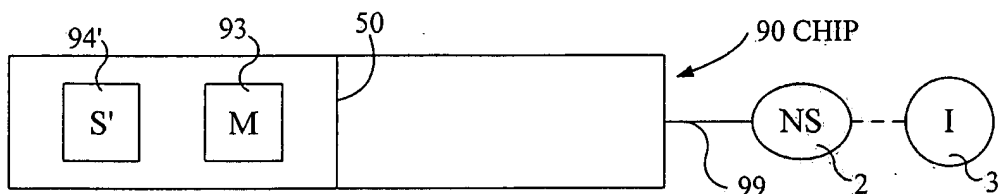


FIG. 10Q

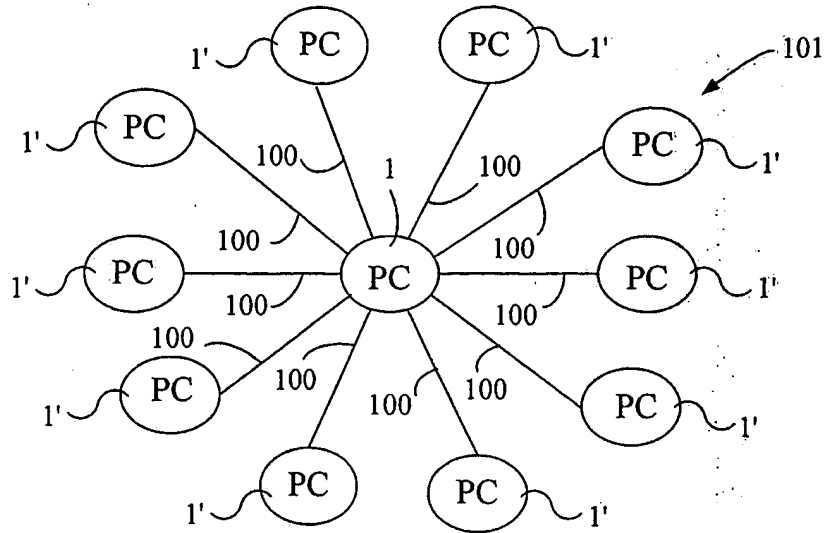


FIG. 11

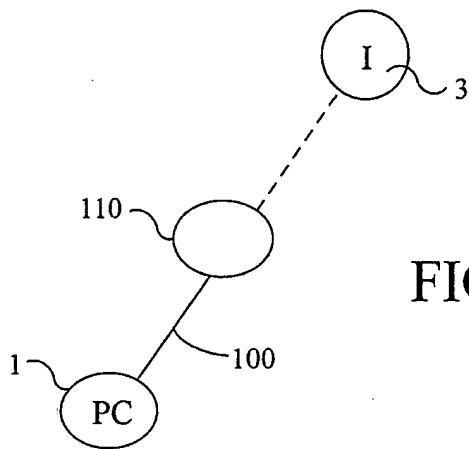


FIG. 12

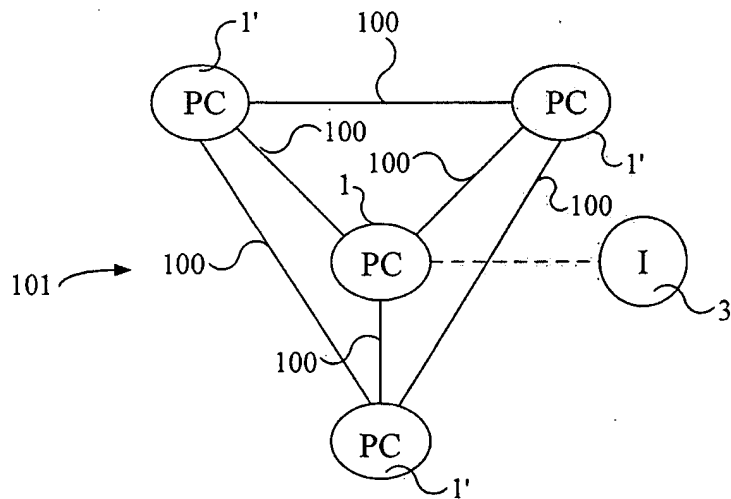


FIG. 13

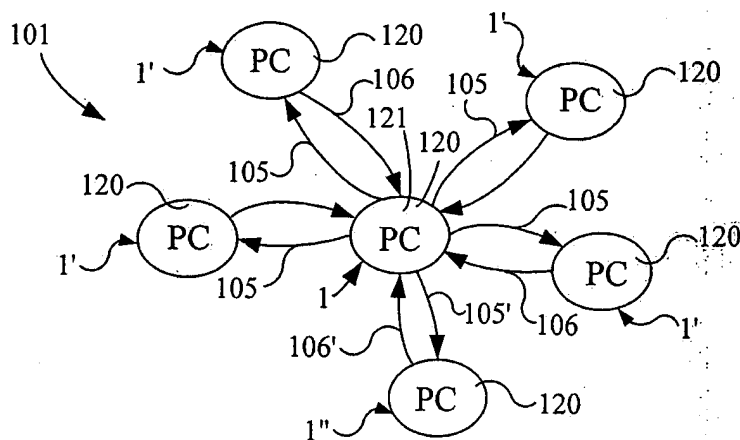


FIG. 14A

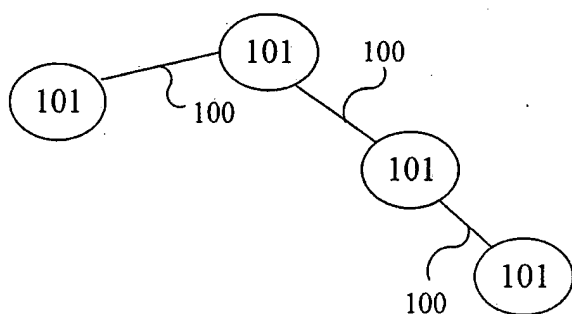


FIG. 14B

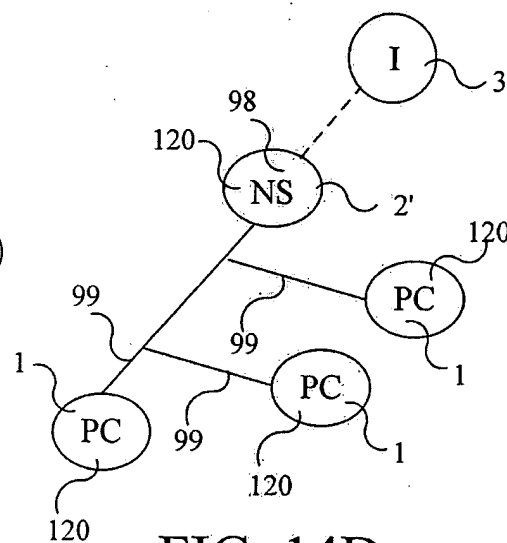


FIG. 14D

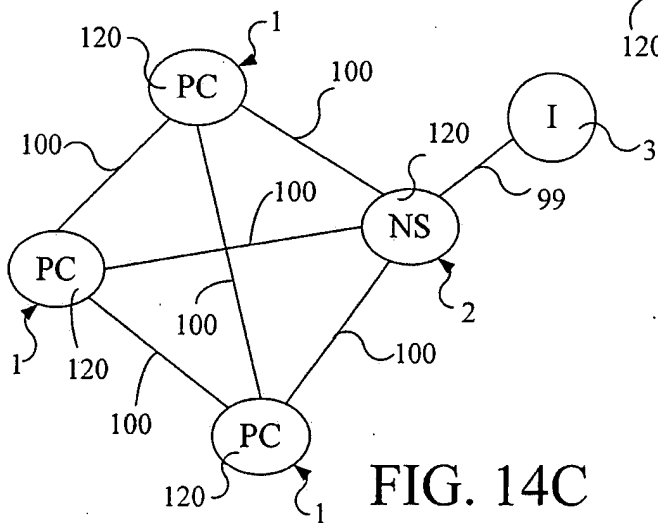


FIG. 14C

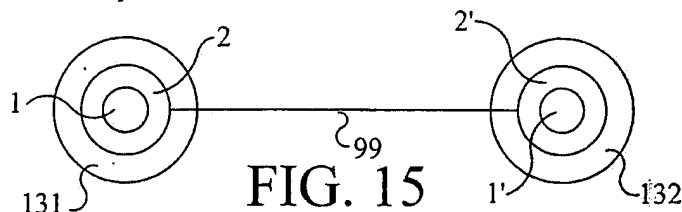


FIG. 15



(PART OF INTERNET OR  
 INTRANET ON OTHER NET)

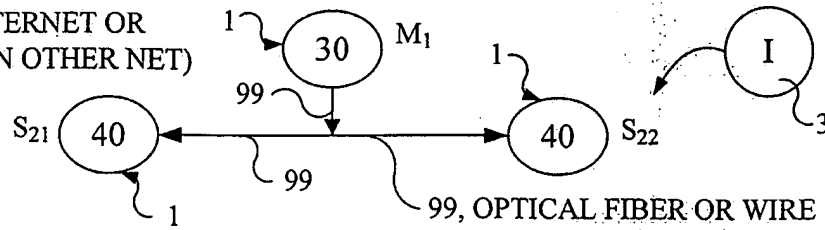


FIG. 16A

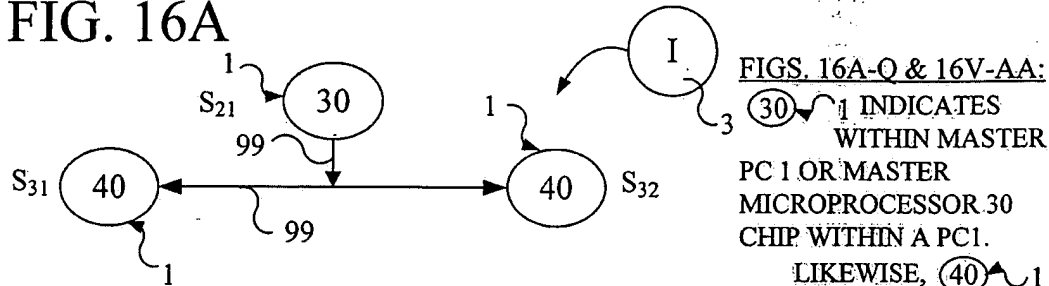


FIG. 16B

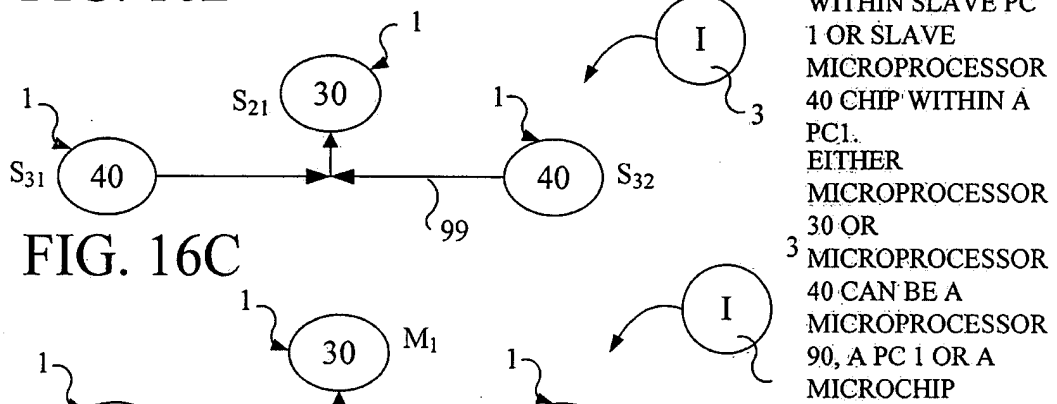


FIG. 16C

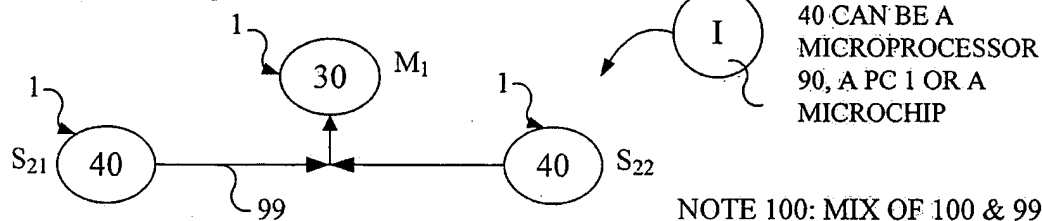


FIG. 16D

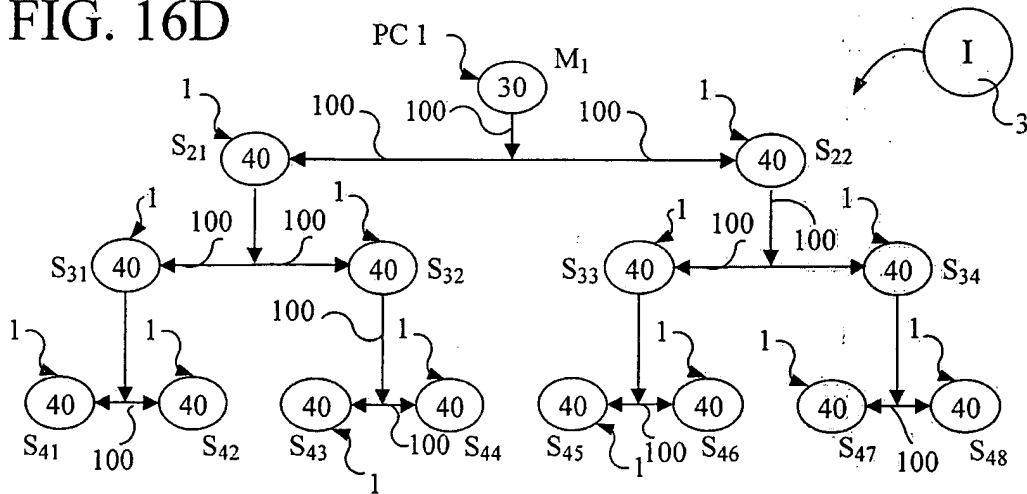


FIG. 16E

FIGS. 16A-Q & 16V-AA:

(30) 1 INDICATES  
 WITHIN MASTER  
 PC 1 OR MASTER  
 MICROPROCESSOR 30  
 CHIP WITHIN A PC1.

LIKewise, (40) 1  
 INDICATES  
 WITHIN SLAVE PC  
 1 OR SLAVE  
 MICROPROCESSOR  
 40 CHIP WITHIN A  
 PC1. EITHER  
 MICROPROCESSOR  
 30 OR  
 MICROPROCESSOR  
 40 CAN BE A  
 MICROPROCESSOR  
 90, A PC 1 OR A  
 MICROCHIP

NOTE 100: MIX OF 100 & 99

FIG. 16F

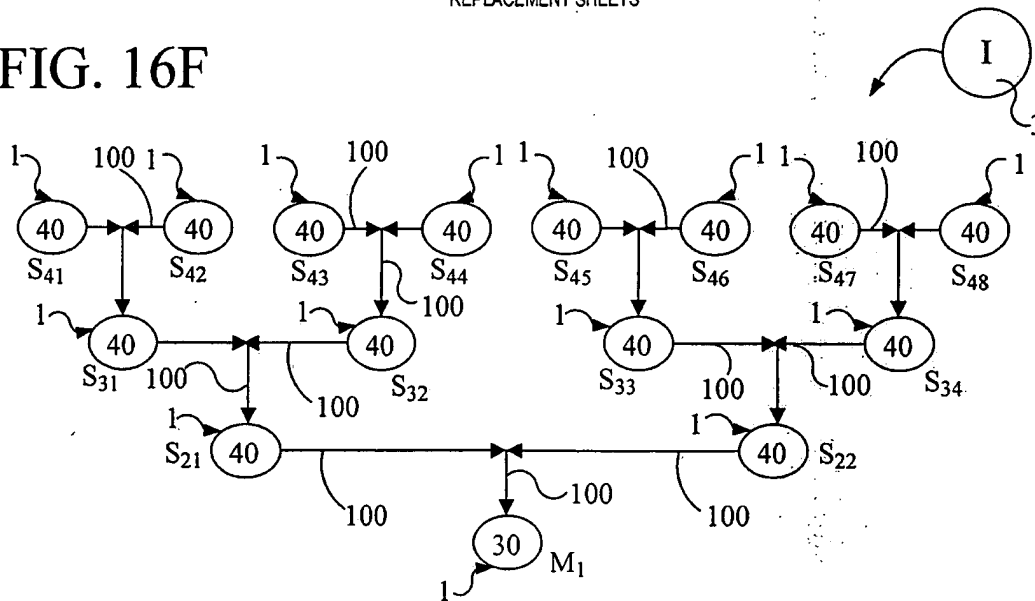


FIG. 16G

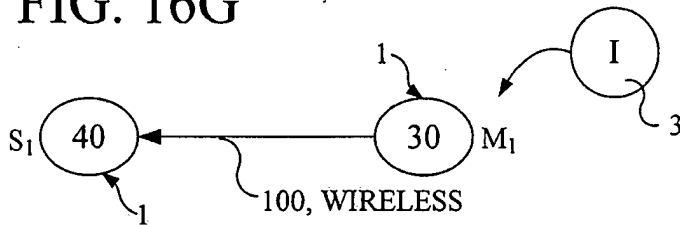
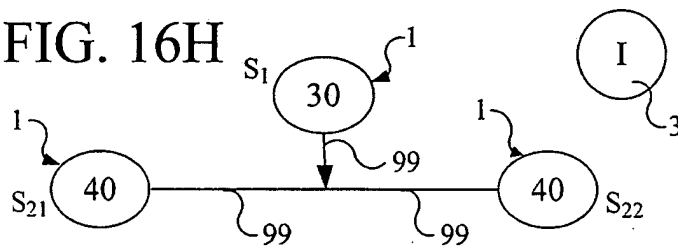
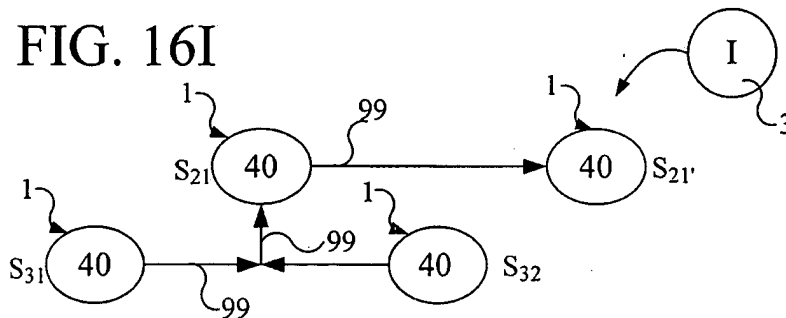


FIG. 16H



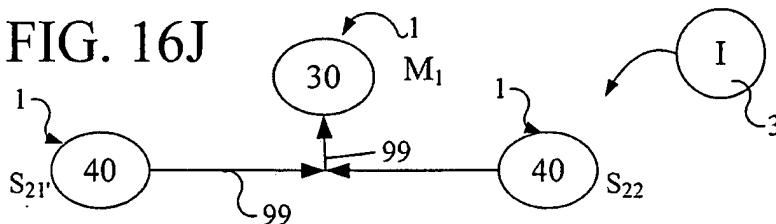
MASTER PC  
 OFFLOADS  
 OPERATIONS TO  
 SLAVE PC 1 WHOSE  
 FUNCTION IS M

FIG. 16I



UNAVAILABLE S21  
 OFFLOADS RESULTS  
 OF S31 & S32 TO  
 AVAILABLE S21',  
 WHICH TAKES OVER

FIG. 16J



LIKE FIG. 16D S21'  
 REPLACES S21



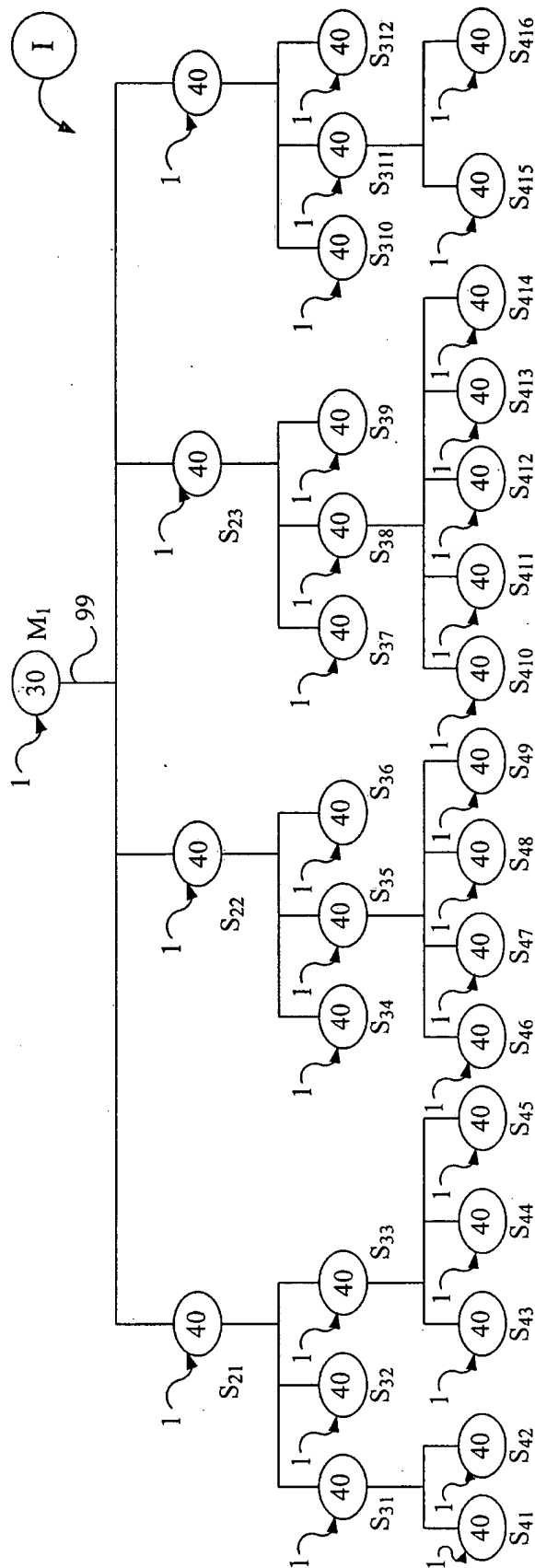


FIG. 16N

FIG. 16O

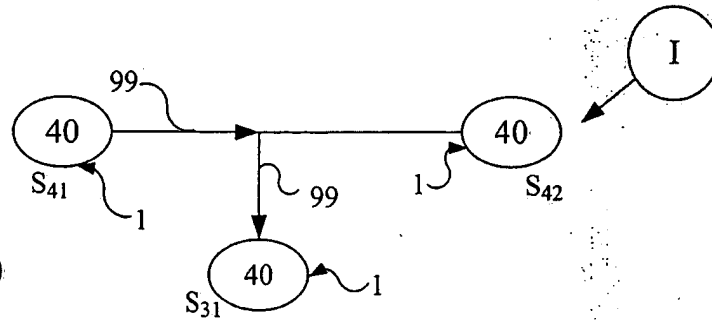


FIG. 16P

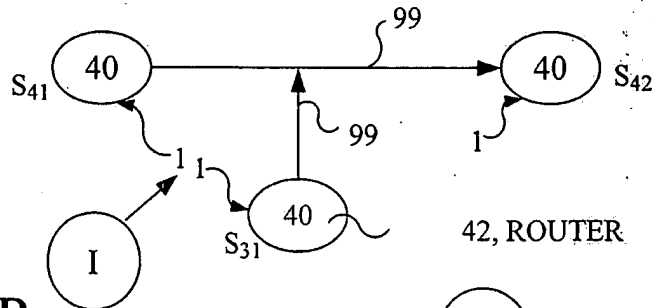
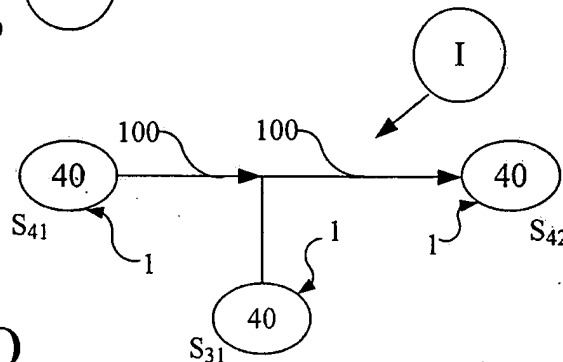
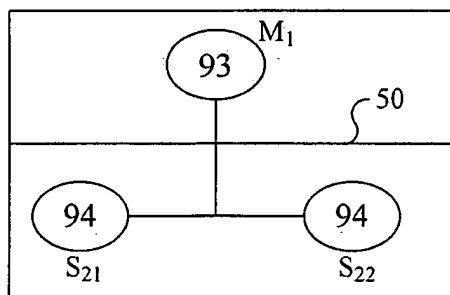


FIG. 16Q



FIGS. 16O-Q  
 ARE SECTIONS  
 OF FIG. 16F NET  
 (Left upper)



LIKE FIG. 10C:  
 "PERSONAL  
 COMPUTER ON A CHIP"  
 (FIGS. 16R-16U)

FIG. 16R

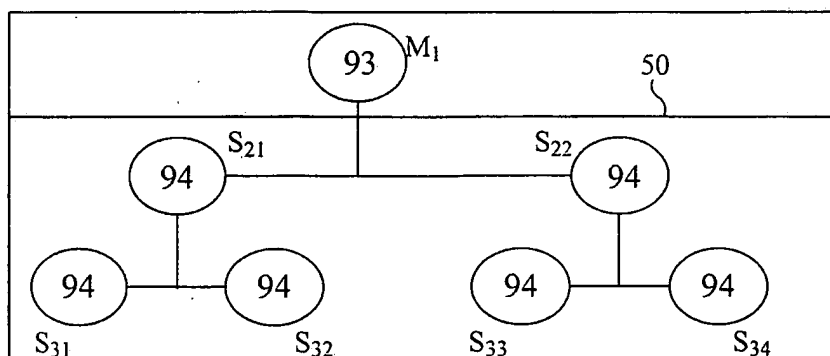


FIG. 16S

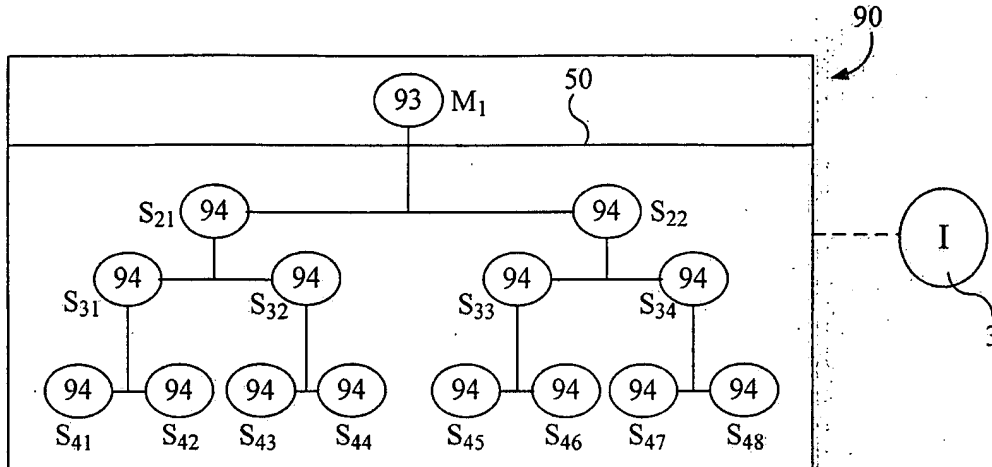


FIG. 16T

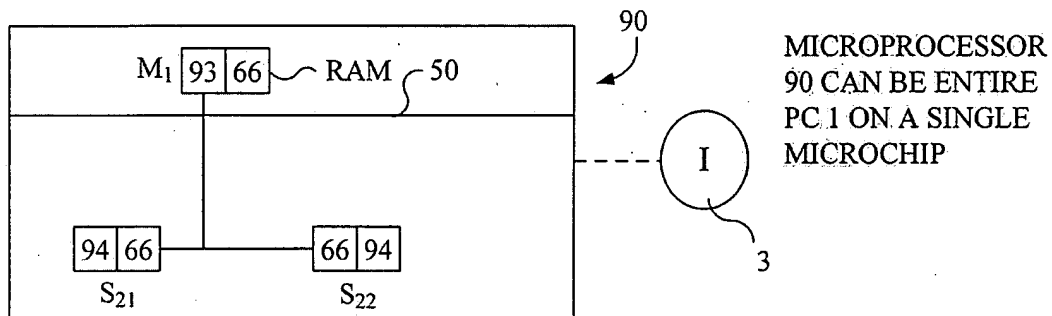


FIG. 16U

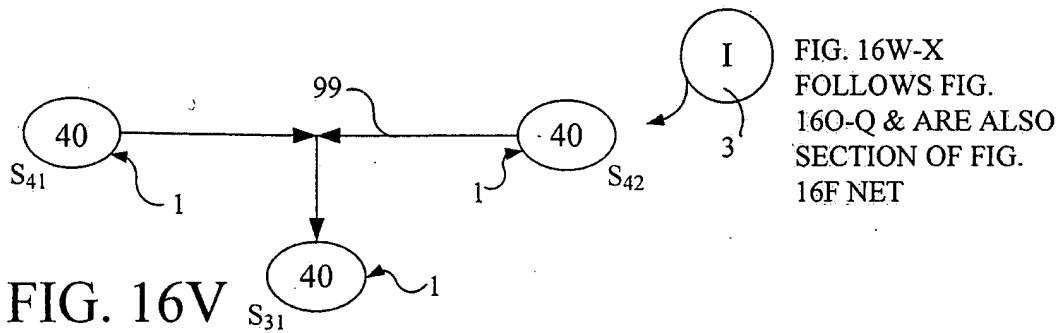


FIG. 16V

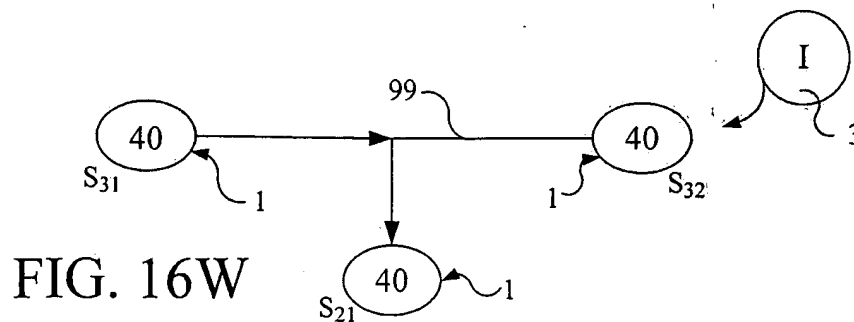
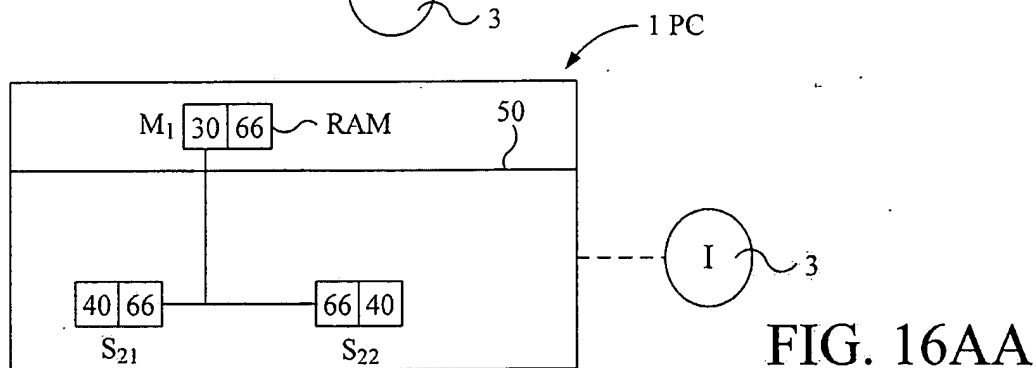
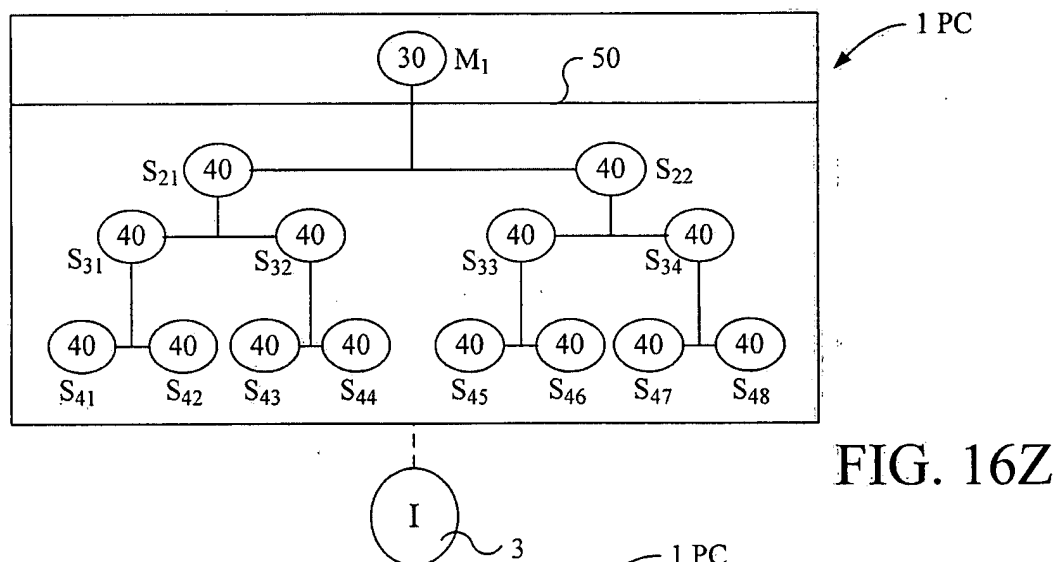
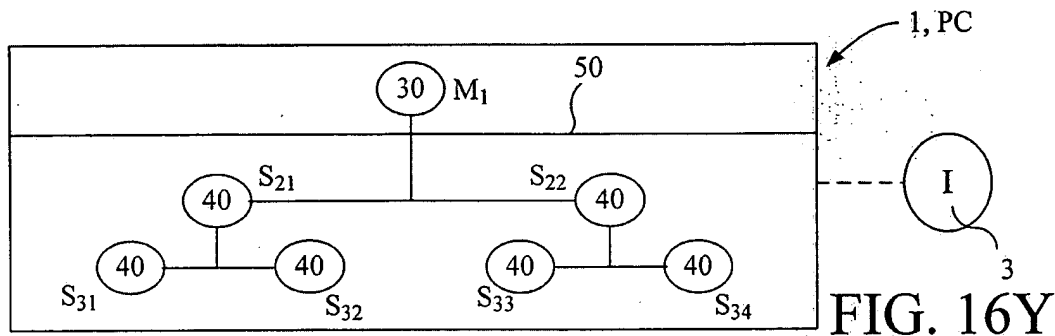
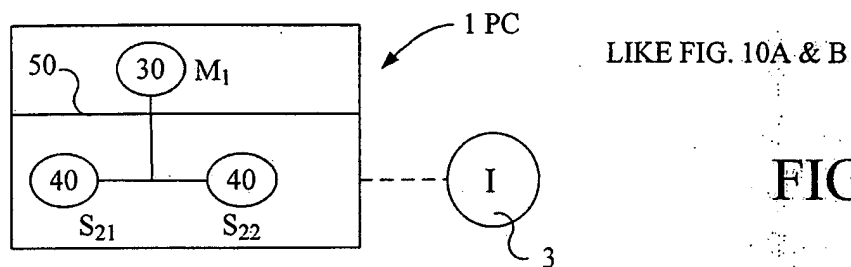
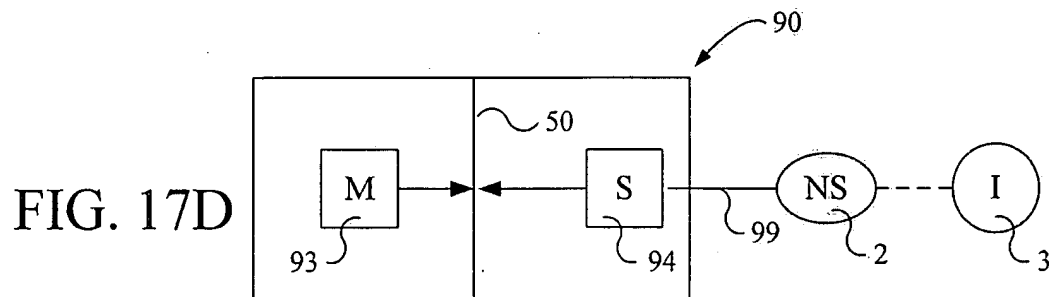
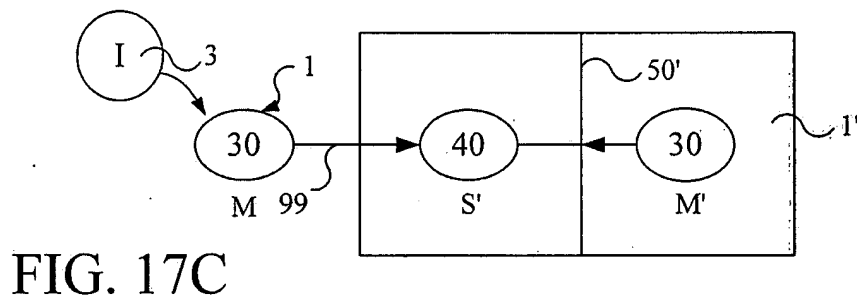
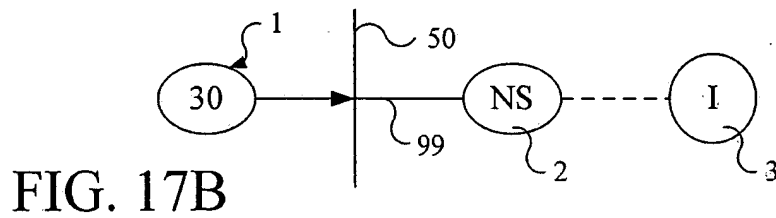
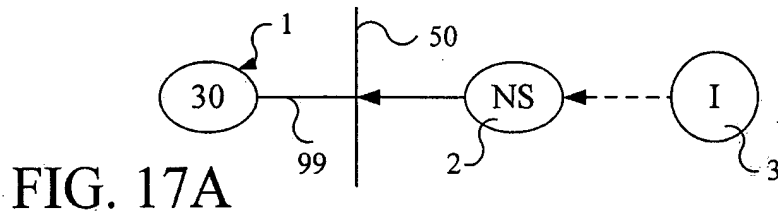


FIG. 16W







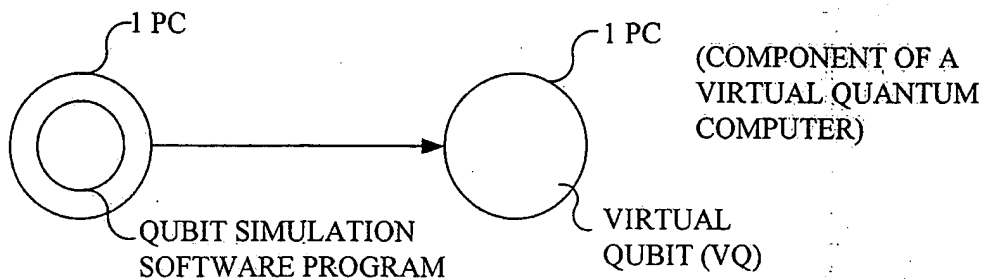


FIG. 18A

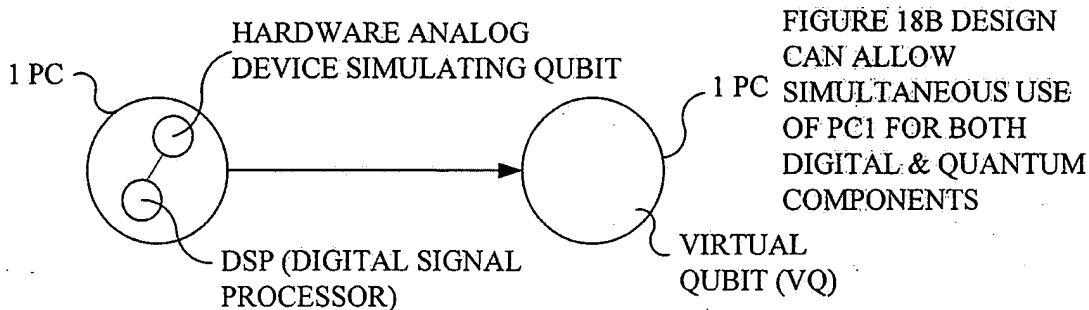


FIG. 18B

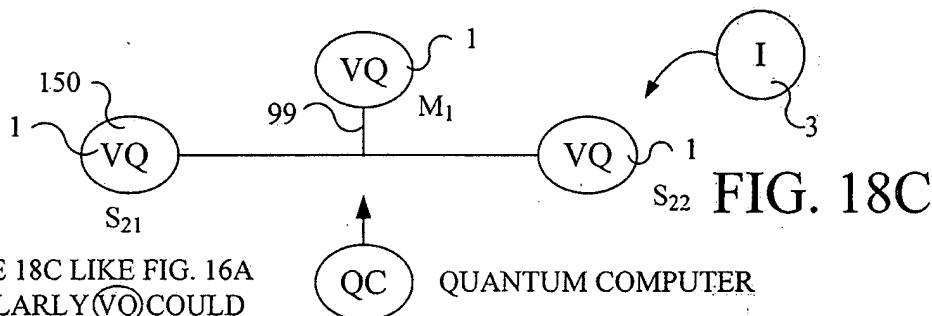


FIG. 18C

FIGURE 18C LIKE FIG. 16A  
 & SIMILARLY VQ COULD  
 BE SUBSTITUTED FOR (30)  
 & 40 IN FIGS. 16B-16Q &  
 16V-16AA AND IN  
 EARLIER FIGURES

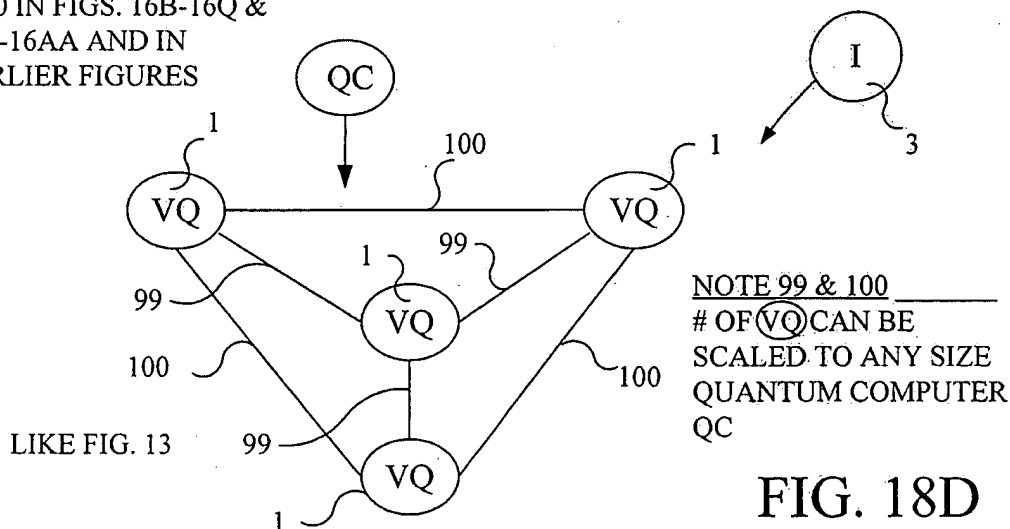


FIG. 18D

